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Applicant(s)

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Information Disclosure Citation  
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09/870,649

Catherwood

Docket Number

Group Art Unit

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068354.1440

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June 1, 2001

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## U.S. PATENT DOCUMENTS

	DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A.	3771146	11/6/73	Cotton et al.	710	260	1/22/73
B.	3784810	12/25/73	Downing	340	172.5	4/26/72
C.	3886524	5/27/75	Appelt	710	110	10/18/73
D.	4025771	5/24/77	Lynch et al.	708	521	3/25/74
E.	4074353	2/14/78	Woods et al.	710	264	3/24/76
F.	4090250	5/16/78	Carson et al.	712	234	9/30/76
G.	4323981	4/6/82	Nakamura	364	749	10/23/78
H.	4379338	4/5/83	Nishitani et al.	708	552	11/21/80
I.	438244	8/9/83	Chu et al.	364	200	3/7/80
J.	4408274	10/4/83	Wheatley et al.	364	200	9/29/80
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L.	4472788	9/18/84	Yamazaki	364	090	8/27/81
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## FOREIGN PATENT DOCUMENTS

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Q.	0 554 917 A2	8/1/793	EP	G06F	9/26	X
R.	96/11443	4/18/96	WO	G06F	15/78	X
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T.	0 992 888	12/04/00	EP	G06F	9/32	X
U.	0 992 889	12/14/00	EP	G06F	9/32	X

## NON-PATENT DOCUMENTS - DOCUMENT (Including Author, Title, Source, and Pertinent Pages)

V.	Moon B I et al.: "A 32-bit RISC Microprocessor with DSP Functionality: Rapid Prototyping" IEEE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, Institute of Electronics Information and Comm. Eng. Tokyo, JP, vol. E84-A no. 5, pages 1339-1347, XP001060025 ISSN: 0918-8508	5/2001
W.	Turley J: "Balancing Conflicting Requirements When Mixing RISC, DSPs" Computer Design, Pennwell Publ. Littleton, Massachusetts, 19, vol. 37, no. 10, pages 46, 48, 50-53, XP000860706 ISSN: 0010-4566	10/1998
X.	Levy M: "Microprocessor and DSP Technologies Unite for Embedded Applications" EDN Electrical Design News, Cahners Publishing Co., Newtown Massachusetts, US, no. Europe, pages 73-74, 76, 78-80, XP000779113 ISSN: 0012-7515	3/24/98

EXAMINER

Nro

DATE CONSIDERED

5/5/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.